# IAR KickStart Kit for LPC11A14

(Part Number: KSK-LPC11A14-JL)



*IAR KickStart Kit*

**A flying start**

Starter kits from IAR Systems provide you with all the tools you need to develop embedded applications right out of the box.

IAR KickStart Kit for LPC11A14 contains all the necessary hardware and software and and allows you to design, integrate and test your applications:

* LPC11A14-SK evaluation board
* IAR J-Link Lite for ARM
* IAR Embedded Workbench for ARM, 16K Kickstart and 30-day evaluation edition
* IAR visualSTATE 20-state evaluation edition
* Example applications made for the evaluation board from IAR Systems
* RTOS board support from IAR Systems' RTOS partner program.

*LPC11A14-SK evaluation board*

*IAR J-Link Lite for ARM*

## LPC11A14-SK evaluation board

Mounted with LPC11A14 (Cortex-M0) device: 32 kB flash, 8 kB SRAM, 4 kB EEPROM; configurable analog/mixed-signal, 50MHz CPU frequency

Debug interface
* 20-pin JTAG/SWD connector (SWD1)
* 10-pin Cortex Debug connector (SWD2)

User interface
* Small LCD – low power 8 character 7 segment display
* Potentiometer - 2 ADC input channels
* Buzzer – 1 DAC output
* User, Wake-up and Reset buttons
* 8 GPIO LEDs

Communication interface
* USB connector - USB/UART bridge
* RS232/485 (DB9) connector
* Extension connectors for all other signals (I²C, SSP etc)
* Power
* Power LED, power via JTAG, USB or external power jack

Others
* 3-axis accelerometer
* Prototyping area
* RoHS compliant

## IAR J-Link Lite for ARM

IAR J-Link Lite for ARM is a small JTAG/SWD hardware debug probe; it connects via USB to the PC host running Windows. IAR J-Link Lite integrates seamlessly into IAR Embedded Workbench and is fully plug-and-play compatible

*IAR Embedded Workbench*

### IAR Embedded Workbench® for ARM - KickStart edition

IAR Embedded Workbench is an Integrated Development Environment with a complete and easy-to-use set of C/C++ cross compiler and debugger tools for professional embedded applications.
• IAR C/C++ Compiler for ARM (16K Kickstart and 30-day evaluation edition)
• Project manager
• Editor
• Linker and librarian tools
• C-SPY® debugger
• Full integration with IAR J-Link

### IAR visualSTATE® - Evaluation edition

IAR visualSTATE is a UML-compliant graphical design environment for reactive systems, with advanced formal verification and validation tools as well as a very powerful code generator.
visualSTATE represents any complex reactive system with UML state machines. The use of state machine diagrams is exceptionally beneficial for control logic oriented applications such as monitoring, metering and control applications where reliability, size and deterministic execution are the main criteria, which makes visualSTATE the ideal design tool for ARM-based projects.